

Attorney's Docket No. 006491.P039

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Andrew L. Norrell et al.

Application No.: 10/072,833

Filed: February 6, 2002

Art unit: 2614

Examiner: Singh, Ramnandan P.

For: LOOP EXTENDER WITH
SELECTABLE LINE TERMINATION
AND EQUALIZATION

Confirmation No.: 3740

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

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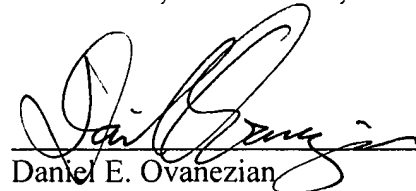
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Respectfully submitted,

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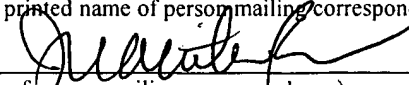

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Complete if Known

Application Number	10/072,833
Filing Date	2/6/02
First Named Inventor:	Andrew L. Norrell
Art Unit	2646
Examiner Name	Singh, Ramnandan P.
Attorney Docket Number	6491.P039

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If known)				
		US-	7,072,385	7/4/06	Hinman et al.	
		US-	20020106012	8/8/02	Norrell et al.	
		US-	20020106076	8/8/02	Norrell et al.	
		US-	6,351,496	2/26/02	Tarraf	
		US-	6,262,972	7/01	McGinn et al.	
		US-	4,334,303	6/8/82	Bertin et al.	
		US-	5,555,274	9/10/96	Sheets et al.	
		US-	4,282,407	8/4/81	Stiefel et al.	
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FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³	Number ⁴ Kind Code ⁵ (if known)				
		JP	61159833	7/19/86	NEC Corporation		
		WO	98/59426	12/30/98	Paradyne Corporation		

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Application Number	10/072,833
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Art Unit	2646
Examiner Name	Singh, Famnandan P.
Attorney Docket Number	6491.P039

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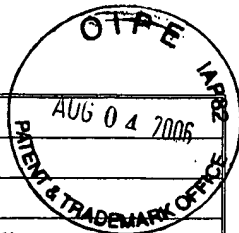
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
		Nilsson, J.W., and Riedel, S.A., "ELECTRIC CIRCUITS", 1996, pages 723-777, Fifth Edition, Addison-Wesley, Reading, MA.	
		Starr, Thomas, et al., "Understanding Digital Subscriber Line Technology," Prentice Hall PTR, Upper Saddle River, NJ, 07458, 1999, ISBN 0137805454, pp. 1-52.	
		Chen, Walter Y., "DSL Simulation Techniques and Standards Development for Digital Subscriber Line Systems", Macmillan Technical Publishing, Indianapolis, Indiana, ISBN 1578700175, pp. 1-33.	
		"Smart Coil™ - The Line conditioner for the digital age! Smart Coils condition copper pairs for deployment of both ADSL (data) and toll-quality voice services on the same line", Charels a registered Trademark of Charles Industries, LTD., 2 pages.	
		"Testing Inter-Winding Capacitance", Rhombus Industries, Inc., Huntington Beach, California 1997, 1 page.	
		"Transformer General Parameters for Telecom Magnetic Component", Delta Products Corporation, Fremont, California, 1 page.	
		"Design Idea DI-61 TinySwitch®-II 3 W Charger: <200 mW No-Load Consumption", Power® Integrations, www.powerint.com , March 2004, 2 pages.	
		Lundahl Transformers, Tube amplifier transformers, OPTs, mains, and interstage transformers", http://www.lundahl.se/tubes.html , April 13, 2004, pages 1-7.	
		"TechEncyclopedia", TechWeb, http://www.techweb.com/encyclopedia/defineterm?term=dsl&x=20&y=5 , April 16, 2004, pages 1-4.	
		Grossner, Nathan R., "The Wide-Band Transformer: Synthesis", and "The Pulse Transformer: Analysis", Transformers for Electronic Circuits, Copyright © 1967, by McGraw-Hill, pages 225-252.	

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